

REMARKS

Favorable reconsideration of this application is respectfully requested.

Claims 1 and 2 are pending in this application. Claims 1 and 2 were rejected under 35 U.S.C. § 112, first paragraph. That rejection is herein traversed as now discussed.

Initially, applicant and applicant's representative wish to thank Examiner Sheets and Supervisory Patent Examiner Mengistu for the interview granted applicant's representative on June 10, 2008. During the interview the outstanding rejection under 35 U.S.C. § 112, first paragraph was discussed in detail. Applicant's representative presented comments as to how claims 1 and 2 were believed to be proper under 35 U.S.C. § 112, first paragraph, and as to how the disclosure was believed to be enabling. The Examiners indicated they would further consider such comments in view of a filed response.

Claims 1 and 2 were rejected as the phrase "all of the electrode patterns have substantially identical shapes and . . . areas of all the electrode patterns are substantially identical" was not clear, and particularly as the specification did not include a clear explanation of the phrase "substantially identical". Applicant traverses that position.

Applicant submits the specification clearly sets forth to one of ordinary skill in the art the meaning of the term "substantially identical".

Applicant first notes Figure 2 in the present specification shows the different electrode patterns P1-Pn that have substantially identical shapes and areas.

The specification also sets forth at several points that by making the areas of the electrode patterns P1-Pn substantially identical, the parasitic capacitances of the switches ASW can also be made approximately equal. That subject matter is noted in the specification for example at page 4, lines 8-12, page 10, lines 9-14, page 10, lines 25-26.

The specification also sets forth the sizes and areas of the electrode patterns P1-Pn are substantially identical such that pattern abnormalities can be easily found by visual

inspection. Such features are noted in the present specification for example at page 4, lines 14-17, and page 11, lines 4-8. In that respect the present specification also compares Figure 2 in the present specification showing the electrode patterns P1-Pn of substantially identical shapes and areas with Figure 3 in which the electrode patterns are a different size, which would be visibly observable.

In view of the foregoing comments, applicant respectfully submits the specification clearly establishes to one of ordinary skill in the art that electrode patterns being of a “substantially identical” shape and area would be well defined to one of ordinary skill in the art to indicate an approximate equalization of parasitic capacitances of switches connected thereto, and of being able to provide a visual indication of differences in length and pattern abnormalities.

Given the above-noted disclosures in the specification, applicant respectfully submits one of ordinary skill in the art would have clearly understood the meaning of the term “substantially identical” with respect to the shapes and areas of the electrode patterns.

In view of the foregoing comments applicant respectfully submits claims 1 and 2 are proper under 35 U.S.C. § 112, first paragraph.

As no other issues are pending in this application, it is respectfully submitted that the present application is now in condition for allowance, and it is hereby respectfully requested that this case be passed to issue.

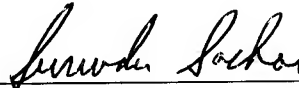
Respectfully submitted,

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